

09/699077 PRO

	Subclass
	Class
	ISSUE CLASSIFICATION

PATENT NUMBER

## U.S. UTILITY Patent Application

O.I.P.E. M 14 SCANNED	PATENT DATE Am 2 Q.A. AA
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APPLICATION NO.	CONT/PRIOR	CLASS 703 438 716	SUBCLASS 15 725	ART UNIT 2123 2812 2133	EXAMINER SHARDIN
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APPLICANTS  
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TITLE  
 Method and apparatus for fault simulation of semiconductor integrated circuit

PTO-2040  
12/99

## ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	(Primary Examiner) _____ (Date)			ISSUE FEE	
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